

JCWSCS 19 FEB 2004

18/2004 16:12 FAX 312 427 6663

LADAS & PARRY

001/002

FILE COPY

Certification of Facsimile Transmission

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being facsimile transmitted to the Patent & Trademark Office to Fax No. (703) 746-9195 on February 18, 2004 (2 pages).

Debra M. Szumowski
Name

Signature

DOCKET: CU-3412

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

APPLICANT: Kiyoshi OOI et al)
SERIAL NO: 10/692,836)
FILING DATE: October 24, 2003) Group Art Unit: 2811
TITLE: CAPACITOR AND MANUFACTURING METHOD)
THEREOF, SEMICONDUCTOR DEVICE AND)
SUBSTRATE FOR A SEMICONDUCTOR DEVICE) Examiner:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED FILING RECEIPT

Dear Sir:

The official filing receipt has been received from the Patent Office in the above application.

There is an error in that the application number of the Japanese priority application is incorrect as shown on the attached copy. Reference is made to page 3 of the Combined Declaration & Power of Attorney initially filed in this application.

Issuance of a corrected filing receipt is respectfully requested.

Respectfully submitted,

February 18, 2004

Date

/37

Brian W. Hameder
Attorney for Applicant

Brian W. Hameder, Reg. 45613
c/o Ladas & Parry
224 South Michigan Avenue
Chicago, Illinois 60604
(312) 427-1300

LADAS & PARRY
UNITED STATES PATENT AND TRADEMARK OFFICE

2004 JAN 27 AM 9:48

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1419
Alexandria, Virginia 22313-1450
www.uspto.gov

APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLM
10/692,836	10/24/2003	2811	942	CU-3412 RJS	17	7	5

28530

LADAS & PARRY

224 SOUTH MICHIGAN AVENUE, SUITE 1200
CHICAGO, IL 60604

CONFIRMATION NO. 9255

FILING RECEIPT



OC000000011754752

Date Mailed: 01/23/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Kiyoshi Ooi, Nagano-shi, JAPAN;
Yasuyoshi Horikawa, Nagano-shi, JAPAN;
Tomoo Yamasaki, Nagano-shi, JAPAN;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2002-314695 10/29/2002

314694

If Required, Foreign Filing License Granted: 01/23/2004

Projected Publication Date: 05/06/2004

Non-Publication Request: No

Early Publication Request: No

Title

Capacitor and manufacturing method thereof, semiconductor device and substrate for a semiconductor device